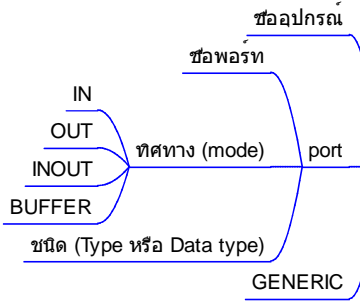


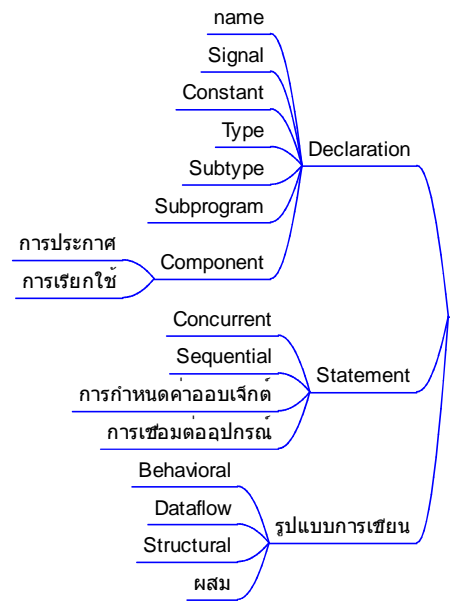
VHDL

โครงสร้าง

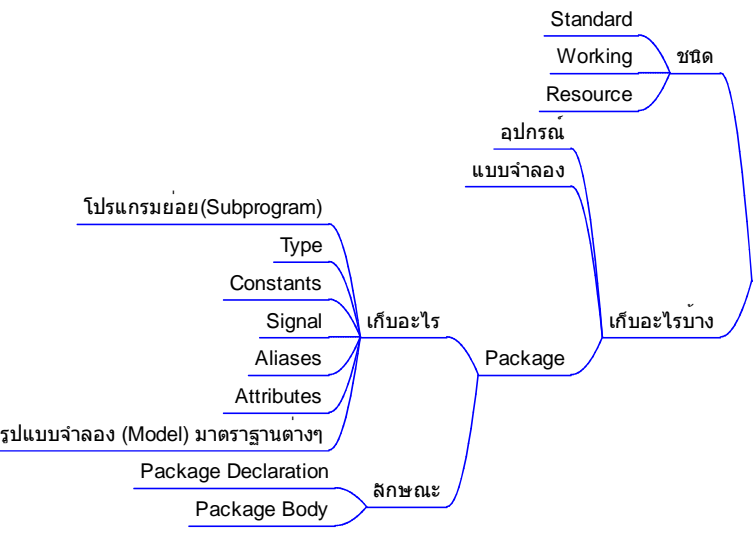
entity



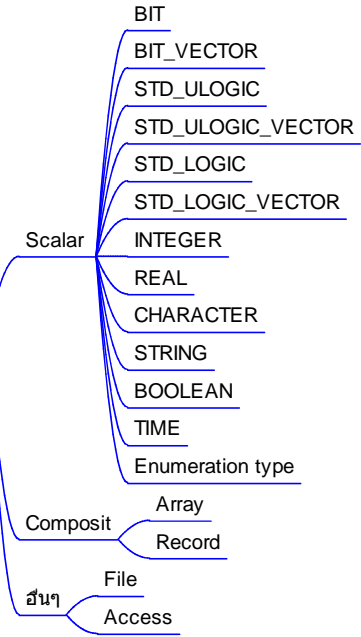
architecture



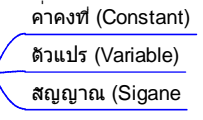
Library



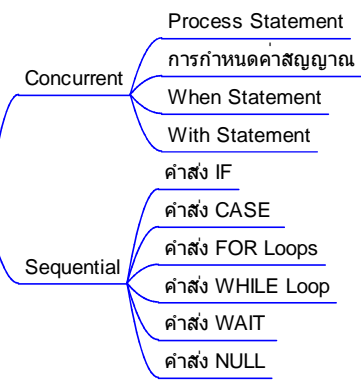
Data type



ออบเจกต์ (Object)



Statement



Operator

